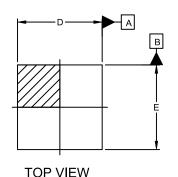
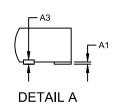
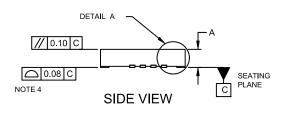


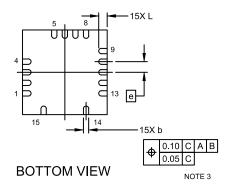
## QFN15 4x4, 0.5P CASE 485FN ISSUE B

**DATE 24 JUL 2019** 









## GENERIC MARKING DIAGRAM\*



XXXXXX = Specific Device Code

A = Assembly Location

L = Wafer Lot Y = Year W = Work Week ■ = Pb-Free Package

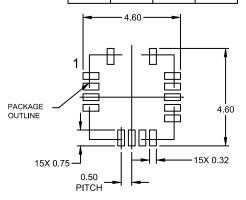
(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present. Some products may not follow the Generic Marking.

## NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- DIMENSION 6 APPLIES TO THE PLATED TERMINAL AND IS MEASURED BETWEEN
   0.15 AND 0.30 FROM THE TERMINAL TIP.
- 4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS		
DIM	MIN.	NOM.	MAX.
Α	0.80	0.85	0.90
A1	0.00	-	0.05
A3	0.10 REF		
b	0.20	0.25	0.30
D	3.95	4.00	4.05
E	3.95	4.00	4.05
е	0.50 BSC		
L	0.30	0.40	0.50



## RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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