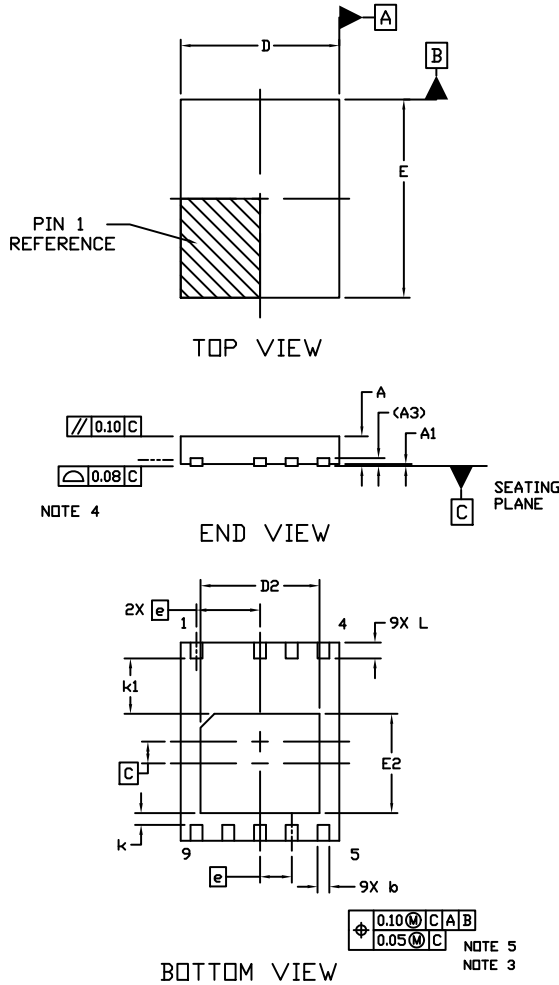


**WDFN9 4x5, 0.8P
CASE 511EF
ISSUE O**

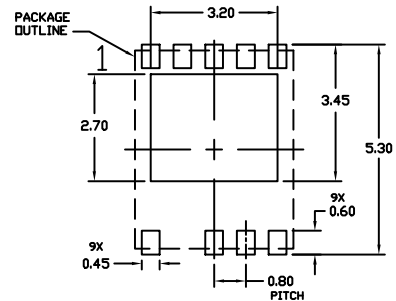
DATE 11 JUN 2020



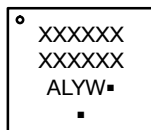
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION b APPLIES TO THE PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO ALL OF THE TERMINALS AND THE EXPOSED PAD.
5. POSITIONAL TOLERANCE APPLIES TO ALL OF THE TERMINALS AND THE EXPOSED PAD.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
A1	0.00	---	0.05
A3	0.20 REF		
b	0.25	0.30	0.35
C	0.55 BSC		
D	3.90	4.00	4.10
D2	2.90	3.00	3.10
E	4.90	5.00	5.10
E2	2.40	2.50	2.60
e	0.80 BSC		
k	0.30 REF		
k1	1.40 REF		
L	0.30	0.40	0.50



GENERIC MARKING DIAGRAM*



- XXXX = Specific Device Code
- A = Assembly Location
- L = Wafer Lot
- Y = Year
- W = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

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