onsemi

IGBT for Automotive Application 650 V, 30 A AFGHL30T65RQDN

Using novel field stop IGBT technology, **onsemi**'s new series of FS4 IGBTs offer the optimum performance for automotive applications. This technology is Short circuit rated and offers high figure of merit with low conduction and switching losses.

Features

- Maximum Junction Temperature: $T_J = 175^{\circ}C$
- Positive Temperature Co-efficient for Easy Parallel Operation
- High Current Capability
- Low Saturation Voltage: $V_{CE(Sat)} = 1.57 V (Typ.) @ I_C = 30 A$
- 100% of the Parts Tested for I_{LM} (Note 2)
- High Input Impedance
- Fast Switching
- Tightened Parameter Distribution
- This Device is Pb-Free and RoHS Compliant

Typical Applications

• E-compressor for HEV/EV, PTC heater for HEV/EV

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-to-Emitter Voltage	V _{CES}	650	V
Gate-to-Emitter Voltage Transient Gate-to-Emitter Voltage	V _{GES}	±20 ±30	V
Collector Current (Note 1) @ $T_C = 25^{\circ}C$ @ $T_C = 100^{\circ}C$	lc	42 30	A
Pulsed Collector Current (Note 2)	I _{LM}	120	А
Pulsed Collector Current (Note 3)	I _{CM}	120	А
Diode Forward Current (Note 1) @ T _C = 25°C @ T _C = 100°C	١ _F	42 30	A
Pulsed Diode Maximum Forward Current	I _{FM}	120	А
Non–Repetitive Forward Surge Current (Half–Sine Pulse, tp = 8.3 ms, T_C = 25°C) (Half–Sine Pulse, tp = 8.3 ms, T_C = 150°C)	I _{FM}	140 100	A
Short Circuit Withstand Time V_{GE} = 15 V, V_{CC} = 400 V, T_C = 150°C	t _{SC}	5	μs
Maximum Power Dissipation @ T _C = 25°C @ T _C = 100°C	P _D	230.8 115.4	W
Operating Junction/Storage Temperature Range	T _J , T _{STG}	–55 to +175	°C
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	ΤL	265	°C

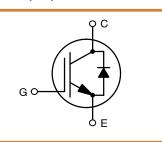
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

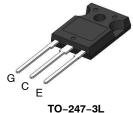
1. Value limited by bond wire.

2. V_{CC} = 600 V, V_{GE} = 15 V, I_C = 90 A, R_G = 75 Ω , Inductive Load, 100% Tested.

3. Repetitive Rating: pulse width limited by max. Junction temperature.

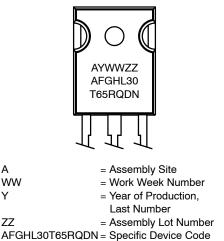
30 A, 650 V, V_{CE(Sat)} = 1.57 V (Typ.)





TO-247-3L CASE 340CX

MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping
AFGHL30T65RQDN	TO-247-3L	30 Units / Rail
	(Pb-Free)	

THERMAL CHARACTERISTICS

Rating	Symbol	Min	Тур	Max	Unit
Thermal Resistance Junction-to-Case, for IGBT	$R_{\theta JC}$	-	0.50	0.65	°C/W
Thermal Resistance Junction-to-Case, for Diode	$R_{\theta JC}$	-	0.92	1.19	
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	-	-	40	

ELECTRICAL CHARACTERISTICS (T_J = $25^{\circ}C$ unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						-
Collector-emitter Breakdown Voltage, Gate-emitter Short-circuited	V _{GE} = 0 V, I _C = 1 mA	BV _{CES}	650	_	-	V
Temperature Coefficient of Breakdown Voltage	V _{GE} = 0 V, I _C = 1 mA	ΔBV_{CES} ΔT_{J}	-	0.58	-	V/°C
Collector-emitter Cut-off Current, Gate-emitter Short-circuited	V_{GE} = 0 V, V_{CE} = V_{CES}	I _{CES}	-	-	30	μΑ
Gate Leakage Current, Collector-emitter Short-circuited	V_{GE} = V_{GES} , V_{CE} = 0 V	I _{GES}	_	_	±400	nA
ON CHARACTERISTICS						
Gate-emitter Threshold Voltage	$V_{GE} = V_{CE}$, $I_C = 30$ mA	V _{GE(th)}	4.30	5.30	6.30	V
Collector-emitter Saturation Voltage		V _{CE(sat)}		1.57 1.88	1.82 -	V
DYNAMIC CHARACTERISTICS						
Input Capacitance	V_{CE} = 30 V, V_{GE} = 0 V, f = 1 MHz	C _{ies}	-	1570	-	pF
Output Capacitance		C _{oes}	-	56	-	-
Reverse Transfer Capacitance		C _{res}	-	7	-	
Gate Resistance	f = 1 MHz	R _g	-	15	-	Ω
Gate Charge Total	V_{CC} = 400 V, I_{C} = 30 A, V_{GE} = 15 V	Qg	-	37	-	nC
Gate-Emitter Charge		Q _{ge}	-	11	-	-
Gate-Collector Charge		Q _{gc}	-	10	-	
SWITCHING CHARACTERISTICS, IND	UCTIVE LOAD		•			
Turn-on Delay Time	$T_{\rm J} = 25^{\circ} {\rm C}, \ {\rm V}_{\rm CC} = 400 \ {\rm V},$	t _{d(on)}	-	18	-	ns
Rise Time	I_{C} = 15 A, R_{G} = 2.5 Ω , V_{GE} = 15 V, Inductive Load	t _r	-	13	-	
Turn-off Delay Time		t _{d(off)}	-	68	-	
Fall Time		t _f	-	104	-	
Turn-on Switching Loss		Eon	-	0.34	-	mJ
Turn-off Switching Loss		E _{off}	-	0.32	-	
Total Switching Loss		E _{ts}	-	0.65	_	1
Turn-on Delay Time	$T_{J} = 25^{\circ}C, V_{CC} = 400 V,$	t _{d(on)}	-	19	-	ns
Rise Time	I_{C} = 30 A, R_{G} = 2.5 Ω , V_{GE} = 15 V, Inductive Load	t _r	-	29	-	1
Turn-off Delay Time		t _{d(off)}	-	61	-	1
Fall Time	1	t _f	-	78	-	1
Turn-on Switching Loss	1	E _{on}	-	0.79	-	mJ
Turn-off Switching Loss	1	E _{off}	-	0.54	-	1
Total Switching Loss	1	E _{ts}	-	1.30	-	1

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise noted) (Continued)

Diode Reverse Recovery Charge

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
SWITCHING CHARACTERISTICS, IN	DUCTIVE LOAD					
Turn-on Delay Time	$T_{\rm J} = 175^{\circ}C, V_{\rm CC} = 400 \text{ V},$	t _{d(on)}	-	18	-	ns
Rise Time	$I_{\rm C}$ = 15 A, $R_{\rm G}$ = 2.5 Ω , V _{GE} = 15 V, Inductive Load	t _r	-	17	-	
Turn-off Delay Time		t _{d(off)}	-	83	-	
Fall Time		t _f	-	196	-	
Turn-on Switching Loss		Eon	-	0.53	-	mJ
Turn-off Switching Loss		E _{off}	-	0.69	-	-
Total Switching Loss		E _{ts}	-	1.22	-	
Turn-on Delay Time	$T_{\rm J} = 175^{\circ}C, V_{\rm CC} = 400 \text{ V},$	t _{d(on)}	-	21	-	ns
Rise Time	I_{C} = 30 A, R_{G} = 2.5 $\Omega,$ V_{GE} = 15 V, Inductive Load	tr	-	37	-	
Turn-off Delay Time		t _{d(off)}	-	72	-	
Fall Time		t _f	-	164	-	1
Turn-on Switching Loss		Eon	-	1.14	-	mJ
Turn-off Switching Loss	-	E _{off}	-	1.09	-	
Total Switching Loss	-	E _{ts}	-	2.23	-	
DIODE CHARACTERISTICS						-
Diode Forward Voltage	I _F = 30 A, T _J = 25°C	V _F	-	1.7	2.10	V
	I _F = 30 A, T _J = 175°C		-	1.74	-	
DIODE SWITCHING CHARACTERIST	ICS, INDUCTIVE LOAD					
Reverse Recovery Energy	I _F = 30 A, dI _F /dt = 1000 A/μs	E _{rec}	-	46	-	μJ
Diode Reverse Recovery Time	V _R = 400 V, T _J = 25°C	T _{rr}	-	39	-	nS
Diode Reverse Recovery Charge		Q _{rr}	-	345	-	nC
Reverse Recovery Energy	I _F = 30 A, dI _F /dt = 1000 A/μs	E _{rec}	-	205	-	μJ
Diode Reverse Recovery Time	V _R = 400 V, T _J = 175°C	T _{rr}	-	85	-	nS
			1	1	-	1

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Q_{rr}

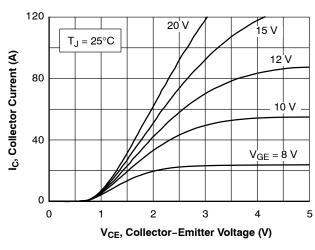
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TYPICAL CHARACTERISTICS





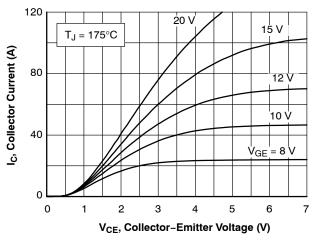


Figure 2. Typical Output Characteristics

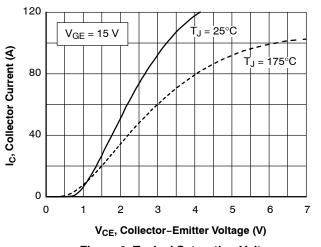


Figure 3. Typical Saturation Voltage Characteristics

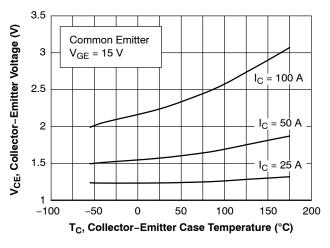


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

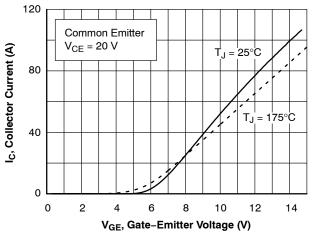
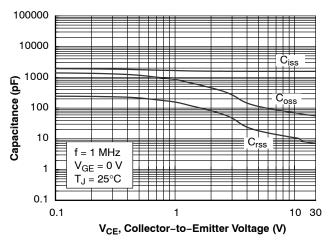


Figure 4. Typical Transfer Characteristics





TYPICAL CHARACTERISTICS (Continued)

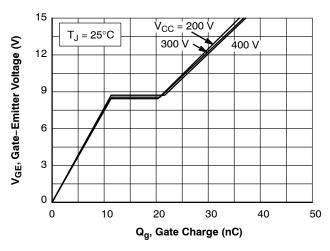
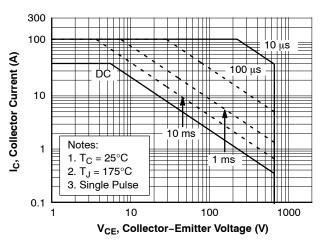
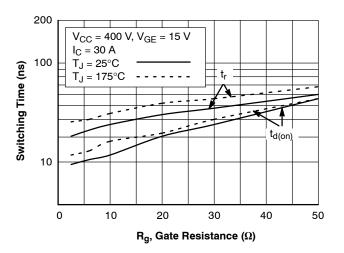


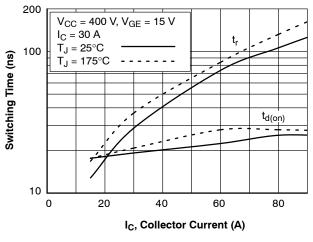
Figure 7. Gate Charge Characteristics

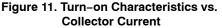












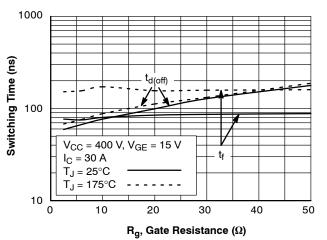
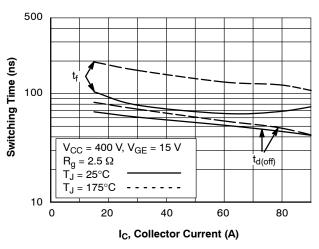
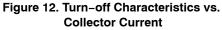


Figure 10. Turn-off Characteristics vs. Gate Resistance





TYPICAL CHARACTERISTICS (Continued)

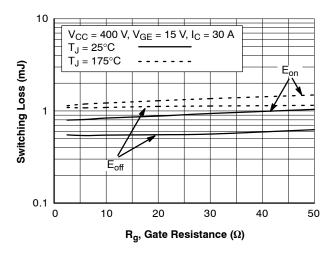


Figure 13. Switching Loss vs. Gate Resistance

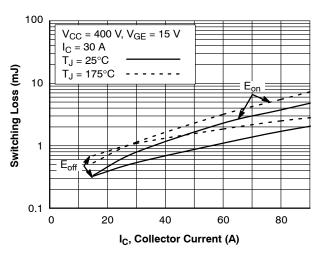


Figure 14. Switching Loss vs. Collector Current

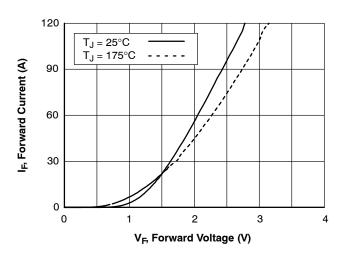


Figure 15. Forward Characteristics

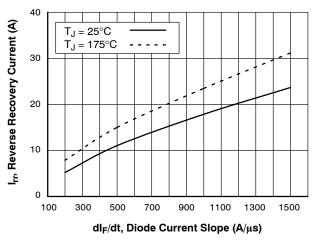
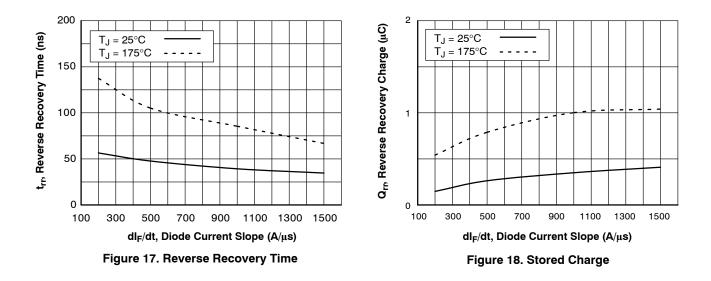


Figure 16. Reverse Recovery Current



TYPICAL CHARACTERISTICS (Continued)

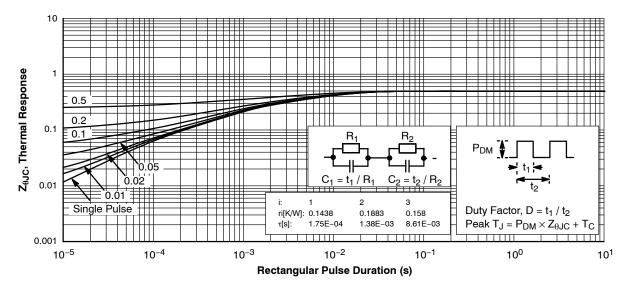


Figure 19. Transient Thermal Impedance of IGBT

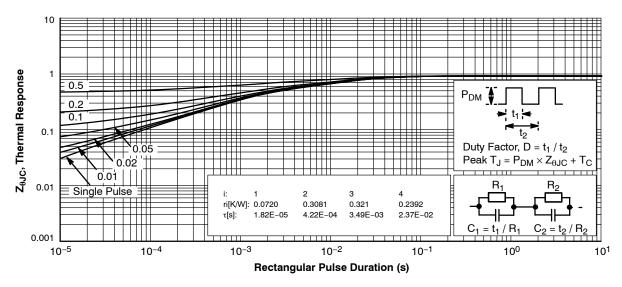
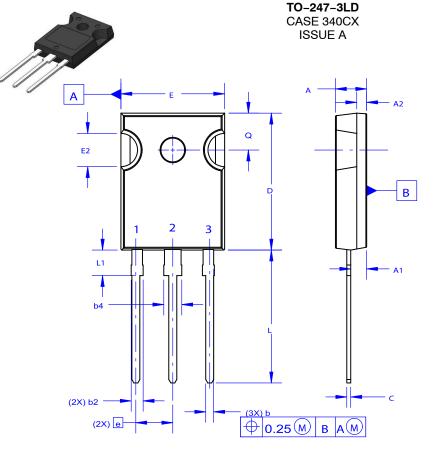


Figure 20. Transient Thermal Impedance of Diode





NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

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GENERIC **MARKING DIAGRAM*** Х



XXXXX	= Specific Device Code
Α	= Assembly Location

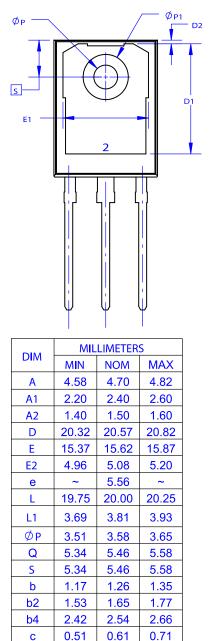
- = Assembly Location
- = Year
- ww = Work Week
- G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ", may or may not be present. Some products may not follow the Generic Marking.

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DATE 06 JUL 2020



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